IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.:	10/560,706	Confirmation No.:	9357
Applicant(s):	Takefumi NISHIMUTA et al.	Group Art Unit:	2894
Filed:	July 24, 2006	Examiner: Alex	ander BELOUSOV
For:	MIS TRANSISTOR AND CMOS TRANSISTOR		
PETITION AND FEE FOR EXTENSION OF TIME (37 C.F.R. § 1.136(a))			
Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Sir:			
 This is a petition for an extension of time for the filing of a divisional patent application. The communication in connection with the matter for which this extension is requested is filed herewith. has been filed on Applicant(s) is/are entitled to Small Entity Status. Statement has already been filed 			
4. a.	Total Months Requested one month two months three months four months five months An extension for months has a identified communication and the from the total fee due for the total fee for this extension (\$), min \$980.00 (total fee due).	e fee paid therefor of \$ I months of extension aus the fee previously p	is deducted now requested. The paid (\$) equals
5. A che	eck in the amount of \$ to cover the extension fee is attached.		

- 6. Charge fee to Deposit Account No. <u>504827</u>, Order No. <u>1004378.52910</u>.
- 7. The Commissioner is hereby authorized to charge any additional fees which may be required by this paper, or credit any overpayment to Deposit Account No. <u>504827</u>, Order No. <u>1004378.52910</u>.

Respectfully submitted, LOCKE LORD BISSELL & LIDDELL LLP

Dated: October 22, 2009

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